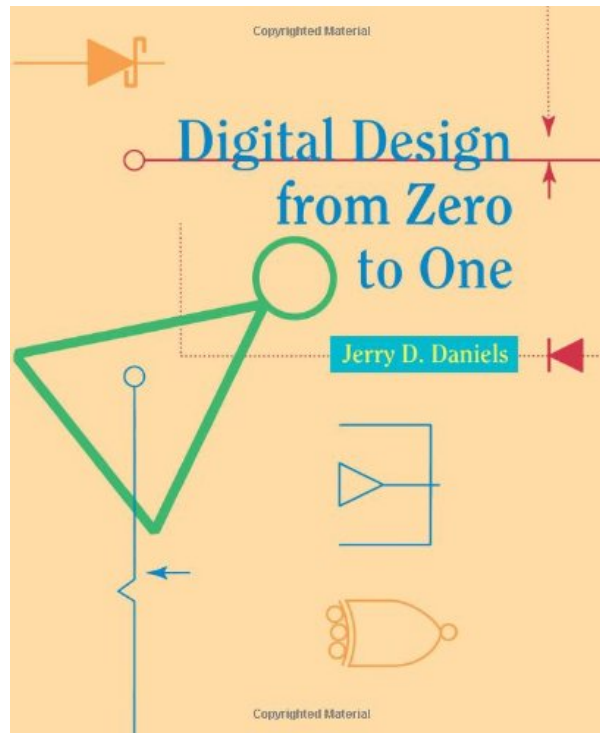
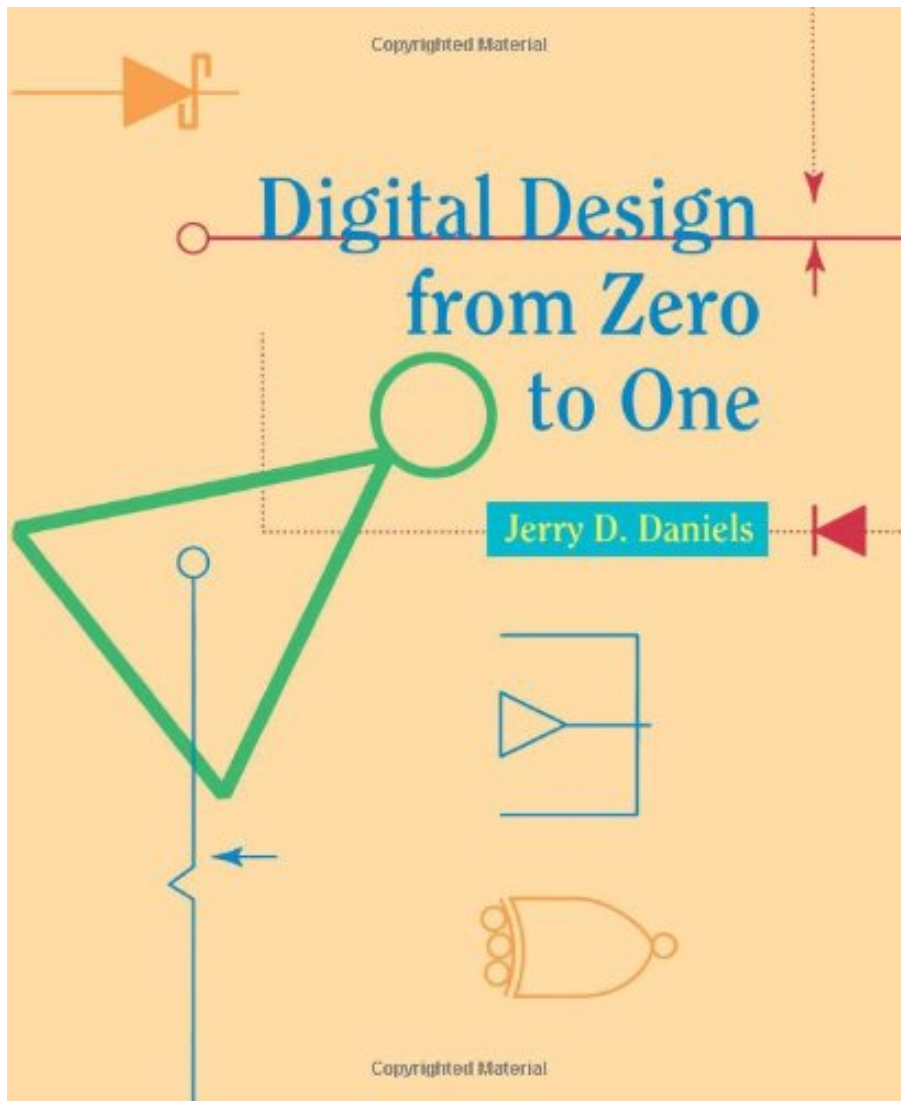


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Review

Lab Manual and Solutions Manual available. -- The publisher, John Wiley & Sons

From the Publisher

This outstanding textbook places greater emphasis on communication memory, interface and fuzzy logic than competing books and contains more graphics. Reflects the realities of the workplace with cutting-edge coverage of field programmable gate arrays, neural networks, PALs, ROMs and the latest memory chips for SRAM and DRAM. Engages the reader with authentic analogies, historical background, applications and occasional humor, providing clarity to the basics of logic design. Includes over 600 diagrams plus numerous examples and problems.

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- Sales Rank: #1407687 in Books
- Published on: 1996-02-16
- Original language: English
- Number of items: 1
- Dimensions: 9.45" h x 1.14" w x 7.64" l, 2.00 pounds
- Binding: Paperback
- 640 pages

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By C. A. Hooks

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That said, I've only read portions of five chapters and have found numerous rather conspicuous errors in both the text and the exercise sets. Some errors involve misrepresentation of k-map covers and demorgan's law; others in the exercise sets aren't so blatantly obvious. If i hadn't already covered this material in classes years ago, i'd certainly be confused. If you need to buy this book for a class, keep this hazard in mind if you ever get stumped.

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